## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Yinxiu LI

Serial Number: Not Yet Assigned

Filed: October 15, 2003 Customer No.: 38834

For: WAVEFORM DISPLAY EQUIPMENT AND WAVEFORM DISPLAYING METHOD

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

October 15, 2003

Sir:

In compliance with 37 CFR 1.56, Applicant(s) call(s) to the attention of the Patent and Trademark Office the reference listed on the attached PTO-1449.

A copy of the reference is enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. <u>50-2866</u>.

Respectfully submitted, WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP

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KH/ll

Enclosures: PTO-1449; Reference(1)

INFORMATION DISCLOSURE STATEMENT PTO-1449			Atty. Do	cket No. 0310	068	Serial No. New Appln.			
			Applicant(s): Yinxiu LI						
			Filing Date: October 15, 2003			Group Art Unit:			
U.S. PATENT DOCUMENTS									
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	AB								
	AC								
	AD								
			FC	DREIGN PAT	TENT DOCUM	ENTS			
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1/1 PLUSPAT · (C) QUESTEL-ORBIT· image

PN - JP8029455 A 19960202 [JP08029455]

PN2 - JP3112230 B2 20001127 [JP3112230]

TI - (A) WAVEFORM ANALYZING DEVICE

PA - (A) YOKOGAWA ELECTRIC CORP

PAO - (A) YOKOGAWA ELECTRIC CORP

IN · (A) TONOSAKA HISANORI

AP - JP16373194 19940715 [\*\*\*1994JP-0163731\*\*\*]

PR - JP16373194 19940715 [1994JP-0163731]

STG · (A) Doc. Laid open to publ. Inspec.

STG2- (B2) Grant. Pat. With A from 2500000 on

AB · PURPOSE: To provide a waveform analyzing device by which waveform display data are automatically classified, and also they are displayed by an optimum format suitable for the number of display.

CONSTITUTION: The waveform analyzing device is provided with a plurality of A/D converters 2(sub 1) to 2(sub n) for separately A/D-converting the analog input signals of a multichannel, an acquistion control circuit 3 for separately controlling the sampling rate and the data length to the A/D converters 2(sub 1) to 2(sub n), a memory 4 for storing the data of the respective channels that have been A/D converted, a display unit 8 for drawing the scale and the waveform, and a means by which the sampling rates and the data lengths of the respective channels are specified for the acquisition control circuit 3 and also the set information including the sampling rates and the data lengths is stored in the memory 4, and the data of the input signal stored in the memory 4 and the set information are read for determining an optimum display format from the number of data and the measuring time of the respective data, so that the scale and waveform data can be drawn on the display unit 8.

- COPYRIGHT: (C)1996,JPO